



- NOTES
- FOR ASSY DWG REFER TO MEC6800-1(A)
 - UNLESS OTHERWISE SPECIFIED ALL RESISTORS ARE IN OHMS, ±5 PERCENT, 1/4 WATT ALL CAPACITORS ARE IN UF ALL VOLTAGES ARE IN DC
 - THERE ARE POSITIONS FOR 28 DECOUPLING CAPS (0.1UF) ON THE BOARD (C5 THRU C32)
 - DENOTES EQUIP. MARKING
 - E1 CONNECTED TO E2 DISABLES TSC AND ENABLES BUS DRIVERS
 - E3 CONNECTED TO E4, NMI NOT FUNCTIONAL ON MPU MODEL A1 AND A2 AND MUST BE GND.
 - E5, E6, AND E7. E5 CONNECTED TO E6 TO USE FIRST 512 BYTES OF ROM. CONNECT E6 TO E7 TO USE ALL OF ROM.
 - E8 CONNECTED TO E10 TO USE PROMS AND CONTROL BUS DRIVERS ON DATA BUS. E8 IS CONNECTED TO E9 FOR EXTERNAL CONTROL OF DATA BUS AND EXPANSION OF SYSTEM.
 - E11 CONNECTED TO E12 TO USE RS-232C INTERFACE.
 - E13 CONNECTED TO E14 TO USE TELETYPE INTERFACE.
 - E15 NORMALLY CONNECTED TO E16
 - R27 REQUIRED ON MPU MODEL B
 - S1 (RESET KNOB) NOT ON THE CARD IN THE EVALUATION SYSTEM MES 6800.

TABLE I

| REF DES | TYPE | +5V TO PIN | GND PIN |
|------------|--------|------------|---------|
| U1 | MC7400 | 14 | 7 |
| U2, U3, U4 | 8T26 | 16 | 8 |
| U5, U6, U7 | | | |
| U8 | MC3459 | 14 | 7 |

TABLE I

| REF DES | TYPE | +5 PIN | -12 PIN | GND PIN | +12 PIN |
|---------|--------|--------|---------|---------|---------|
| U15 | MC7430 | 14 | | 7 | |
| U18 | MC8602 | 16 | | 8 | |
| U21 | MC7437 | 14 | | 7 | |
| U29 | MC1489 | 14 | | 7 | |
| U30 | MC1486 | 1 | | 14 | |

*Rev. Stratex Marketing Europe
20-10-78

SCHEMATIC DIAGRAM
MPU EVALUATION
COMPUTER SYSTEMS ENGRS.

MOTOROLA INC.
Semiconductor Products Division
MES 6800-1(A)S

DATE: 27 MAY 74
BY: [Signature]

REVISION: 1