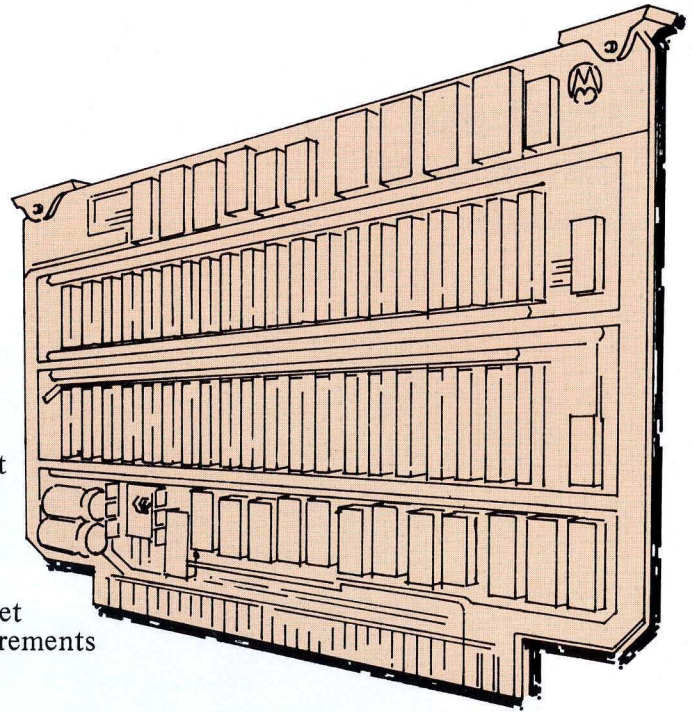




**MOTOROLA**

# MEX68RR EROM/RAM Module



- Up to 16,384 x 8 bits of ROM/PROM memory in four 4K byte arrays
- Utilizes user-programmed 1024 x 8 bit or 512 x 8 bit ROM/PROM devices (four devices per array)
- Switch selectable base memory address for each memory array
- Switch selectable read enable for each installed ROM and read disable for each unused ROM socket
- Up to 512 x 8 bits of RAM memory in 128 byte increments
- TTL voltage compatible
- Bus drive capability

The MEX68RR EROM/RAM Module provides the EXORciser with up to 16,384 bytes of ROM/PROM memory and up to 512 bytes of RAM memory. Four sockets accommodate up to four MCM6810 128 x 8 bit RAM memory devices. The RAM memory is assigned to memory locations 0000<sub>16</sub> through 01FF<sub>16</sub> (the bottom of the EXORciser's memory). However, this array consumes the lower 1024 bytes of the EXORciser's memory (0000<sub>16</sub> through 03FF<sub>16</sub>) although the maximum memory capability is 512 bytes.

The ROM memory section is organized into four memory arrays with four sockets per array; therefore each array is capable of mounting up to 4K bytes of ROM or PROM memory. The user installs his programmed ROMS into the desired sockets, and selects the address for each array by proper setting of each array's base memory address switch.

The 16 individual ROM read switches permit the user to enable the ROM/PROM devices to be read and disable the sockets not being used. Unless disabled by this switch, each ROM/PROM socket occupies 1024 bytes of memory whether the device mounted in the socket is a 512 byte or 1024 byte device. The memory locations assigned to the disabled sockets may be assigned to other memory or peripheral devices in the user's system.

## Specifications

(Note: Positive current flow is defined as flowing into the terminal, negative current flow as flowing from the terminal.)

### Memory Mounting Capability

RAM  
ROM

Up to four MCM6810 or equivalent RAM devices (up to 512 bytes)  
Up to 16 MCM68708 or equivalent EROM devices (up to 16K bytes)

### Memory Organization

RAM  
ROM

Up to 512 x 8 bits organized into one array. (The array consumes 1024 x 8 bits of memory.)  
Up to 16,384 x 8 bits organized into four arrays (four sockets per array), with up to 4096 x 8 bits per array. Unless disabled, each socket consumes 1024 x 8 bits of memory whether it contains a 512 x 8 bit or 1024 x 8 bit ROM/PROM device.

### Input Signals

Logic "0"  
Logic "1"

TTL voltage compatible  
0.0-0.85 V (-200  $\mu$ A max at 0.4 V)  
2.0-5.25 V (25  $\mu$ A max at 5.25 V)

### Data Bus

Input Logic "0"  
Input Logic "1"  
Output Logic "0"  
Output Logic "1"

Three-state TTL voltage compatible  
0.0-0.85 V (-200  $\mu$ A max at 0.4 V)  
2.0-5.25 V (25  $\mu$ A max at 5.25 V)  
0.5 V max at 40 mA through a resistor to V<sub>CC</sub>  
2.6 V min at -10 mA through a resistor to ground

Output Off-State Leakage Current

100  $\mu$ A max at 2.6V

### Operating Temperature

0 to 70° C

### Power Requirements

+5 Vdc at 1A max  
+12 Vdc at 1A max  
-12 Vdc at 1A max

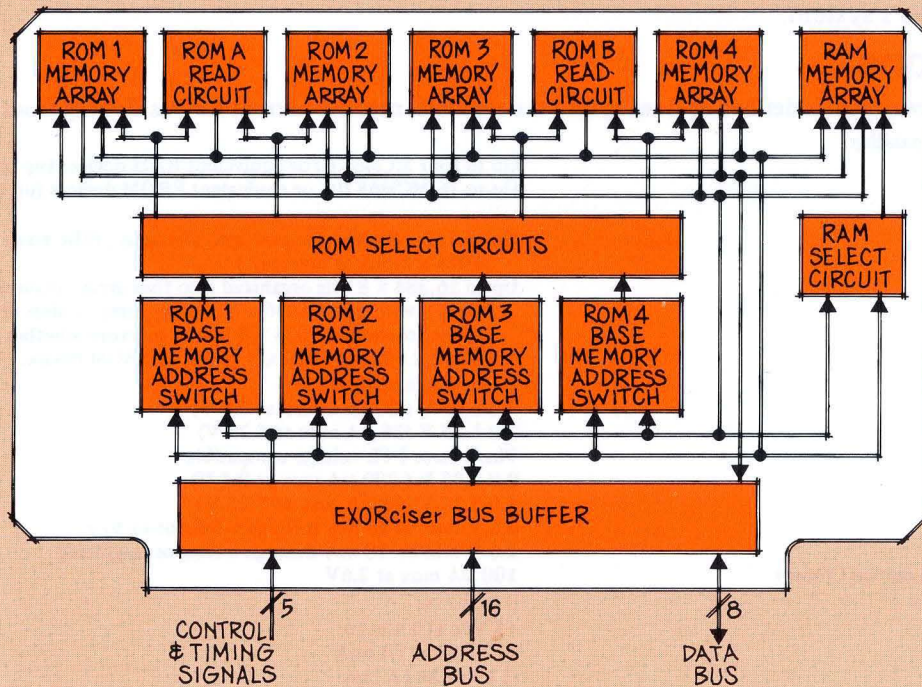
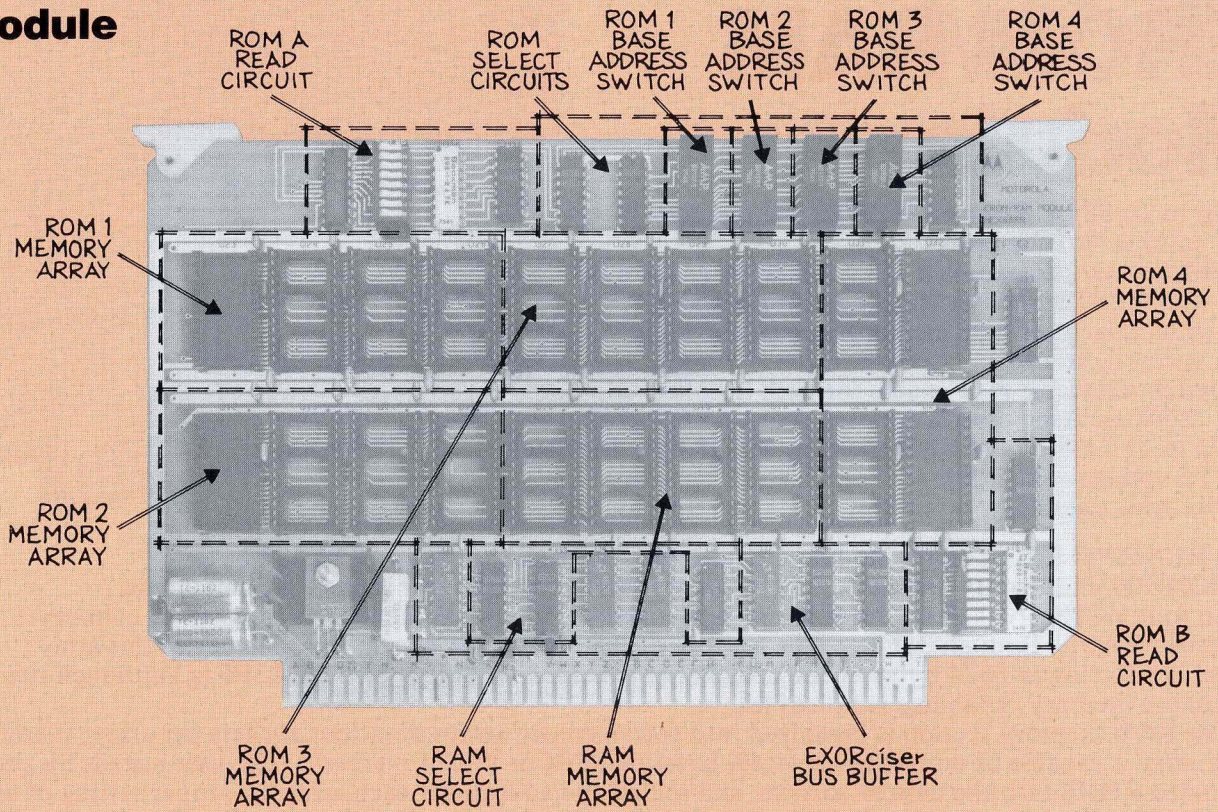
### Physical Dimensions

W x H x T

9.75 x 5.75 x 0.062 in.

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Option



**MOTOROLA Semiconductor Products Inc.**

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